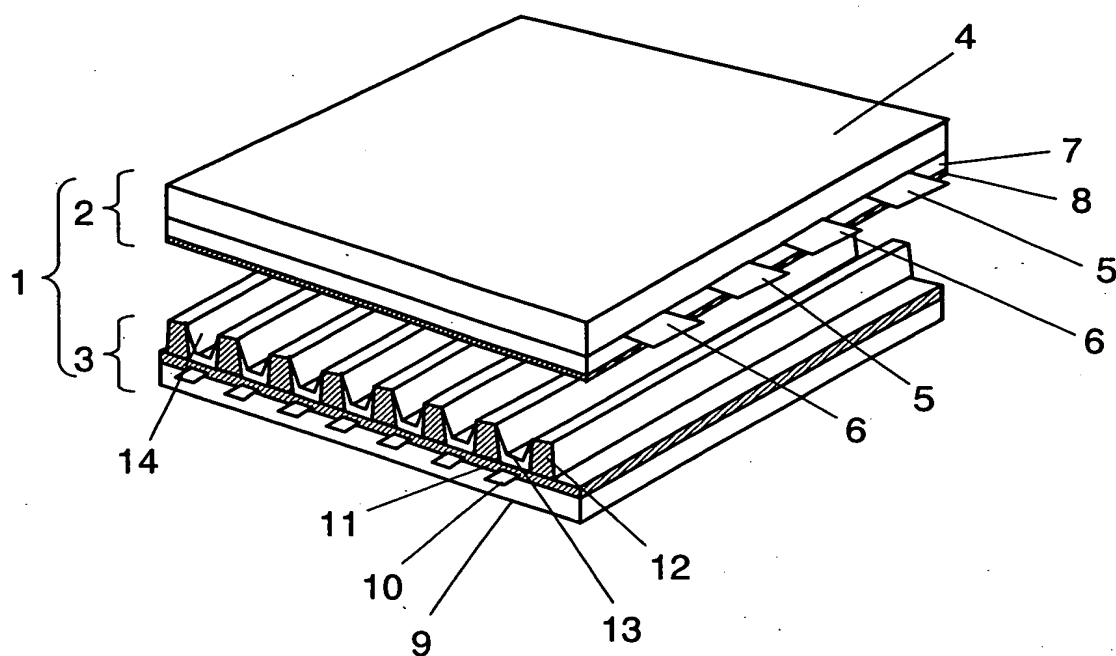
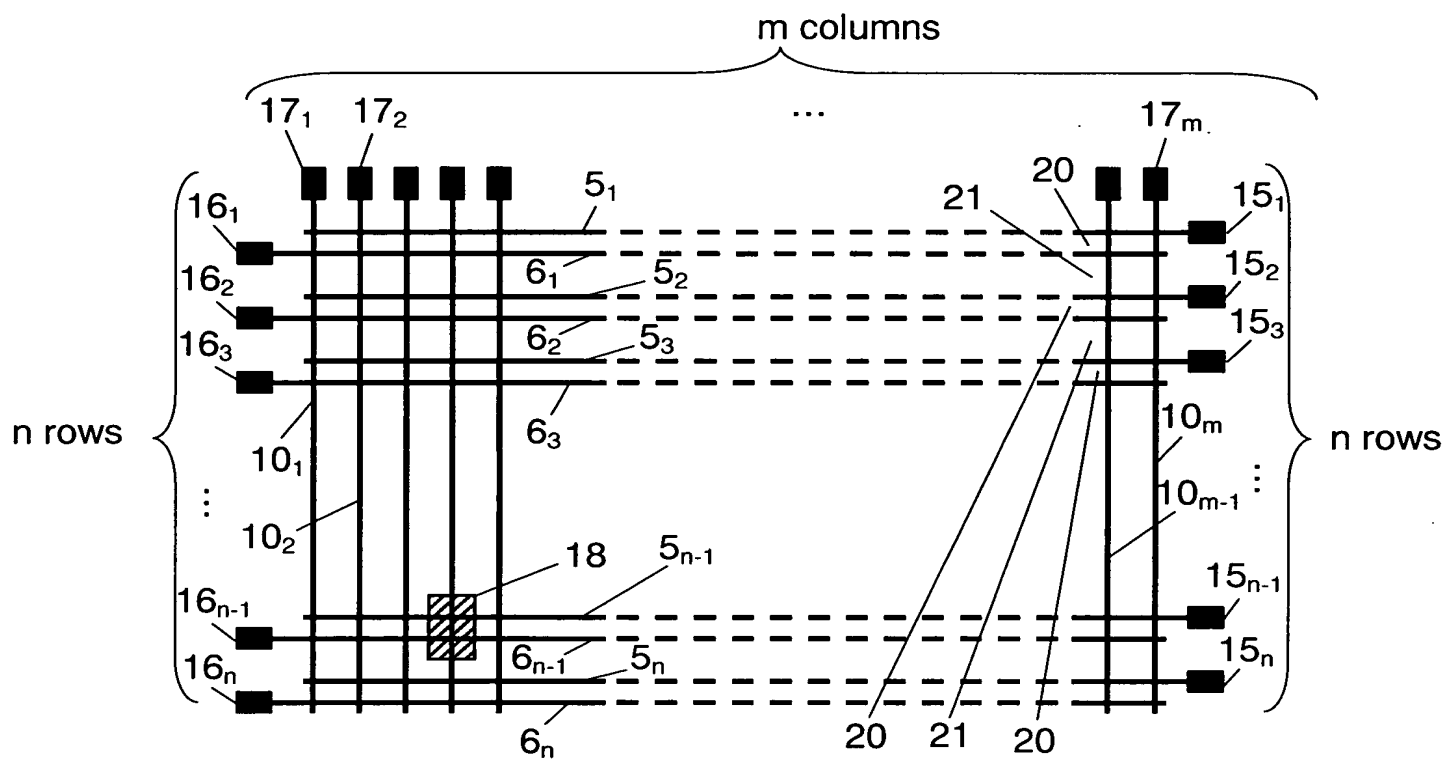


FIG. 1



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FIG. 2



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FIG. 3A

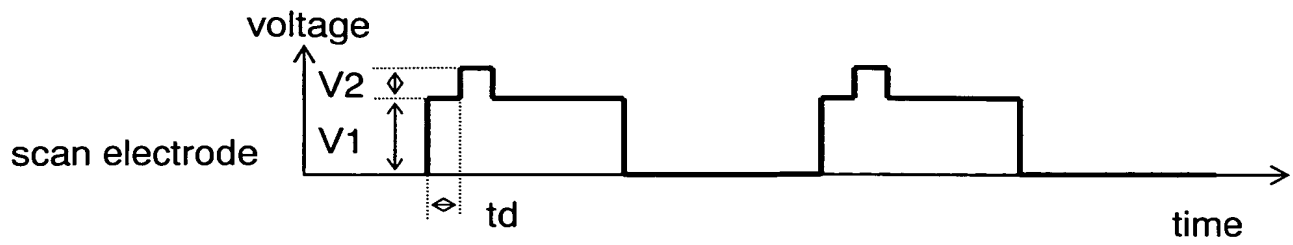


FIG. 3B

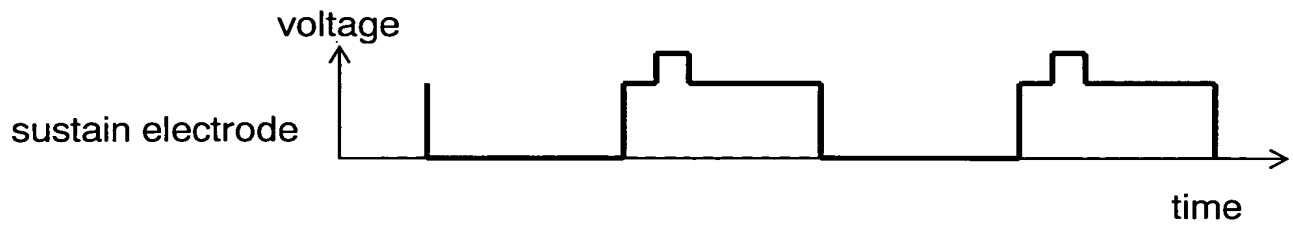


FIG. 3C

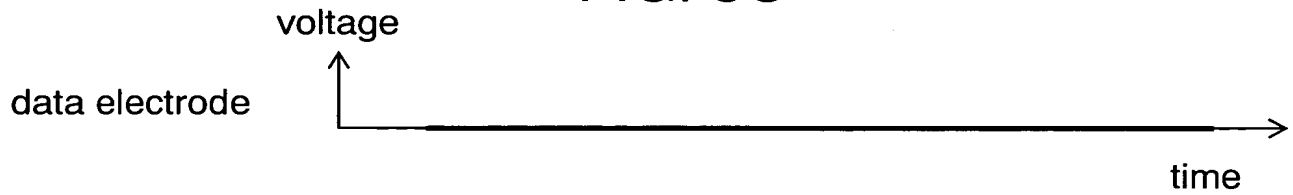


FIG. 3D

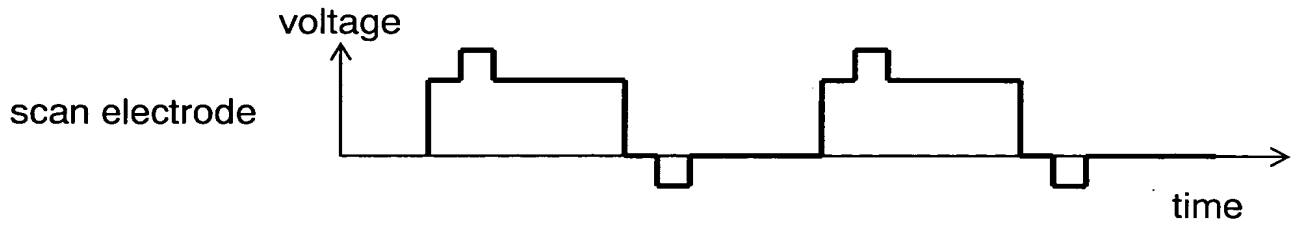
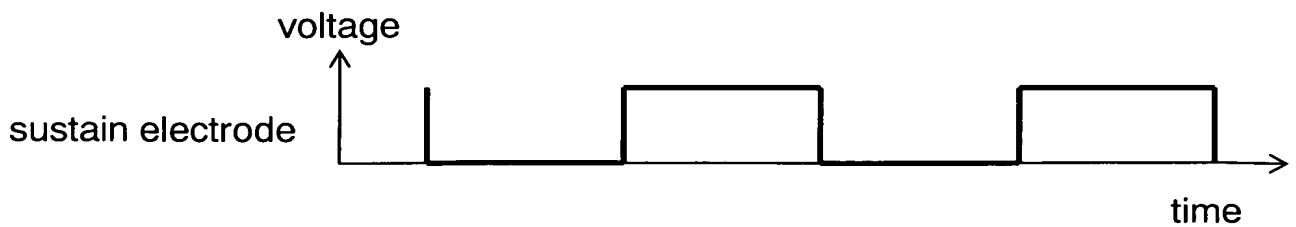


FIG. 3E



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FIG. 4A

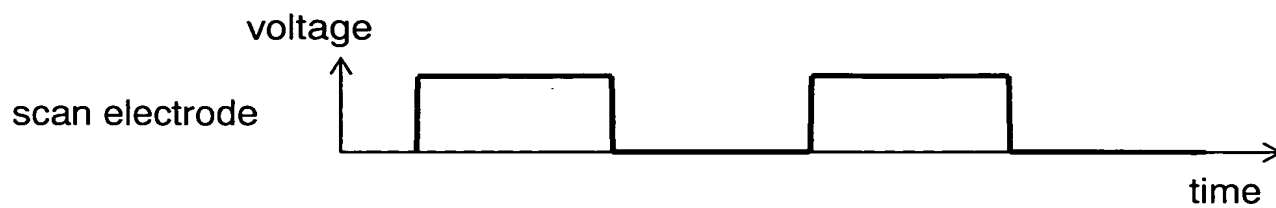


FIG. 4B

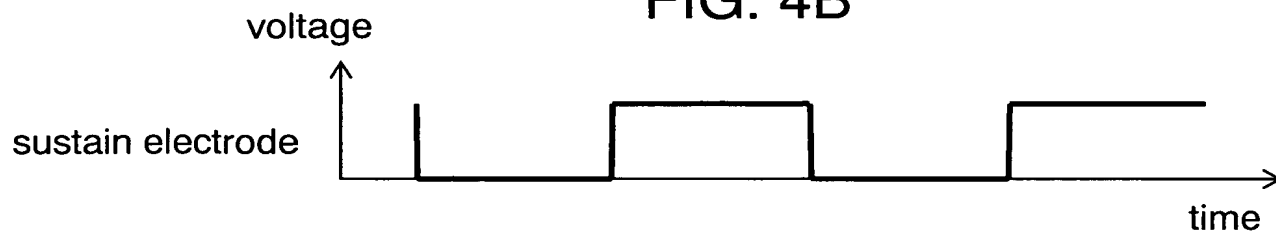


FIG. 4C

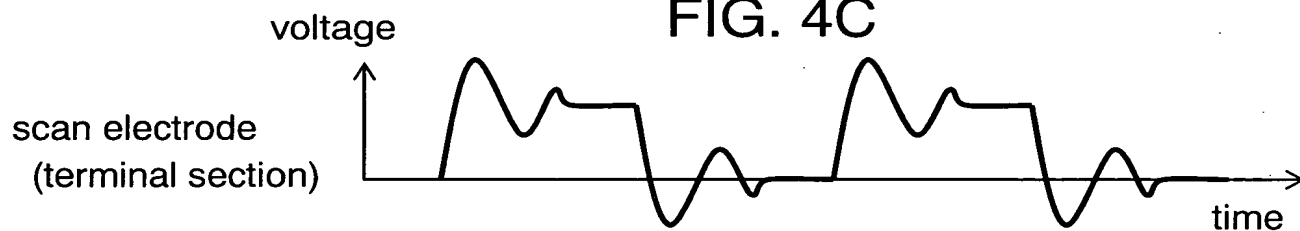


FIG. 4D

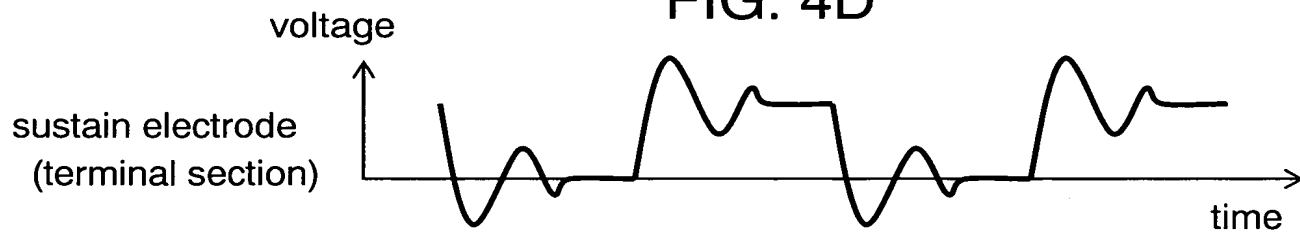
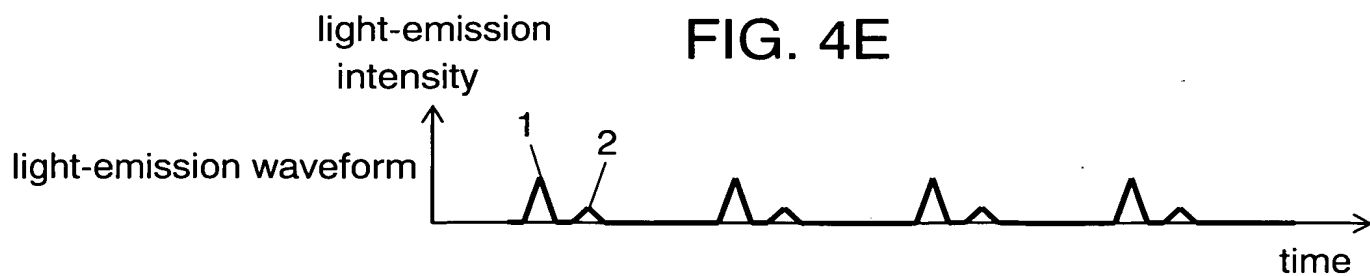


FIG. 4E



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FIG. 5A

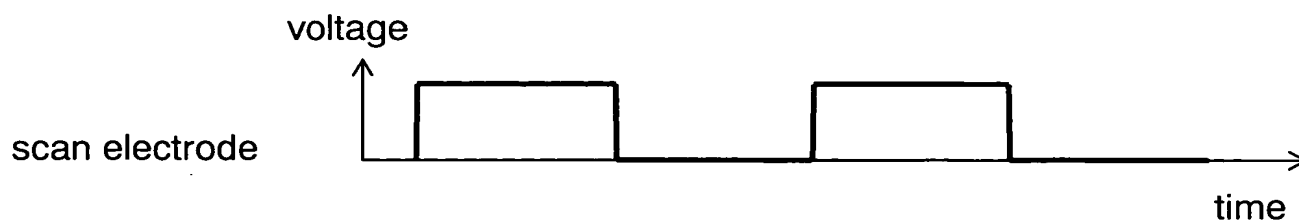


FIG. 5B

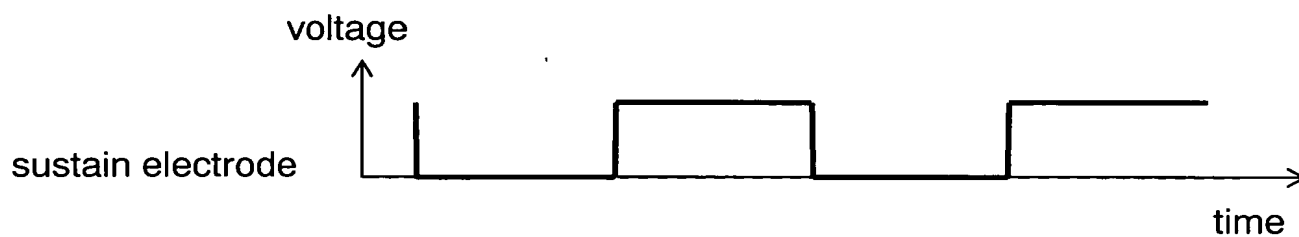


FIG. 5C

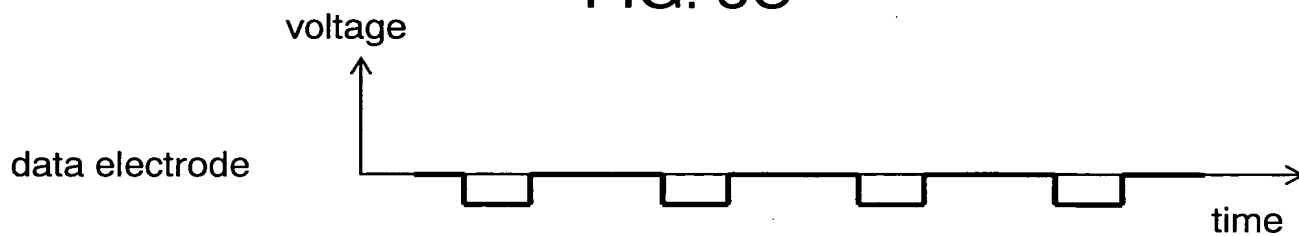
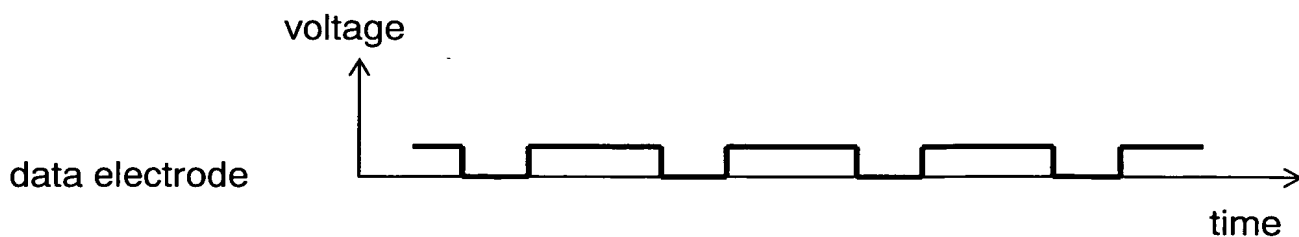


FIG. 5D



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FIG. 6A

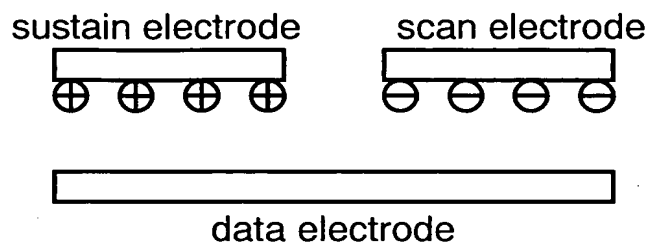


FIG. 6B

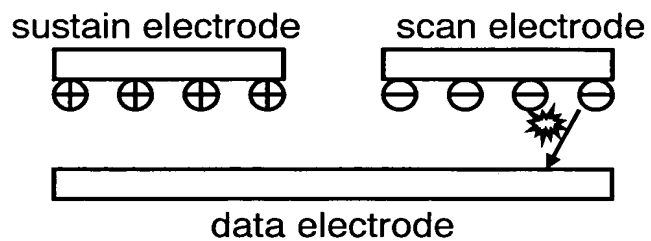


FIG. 6C

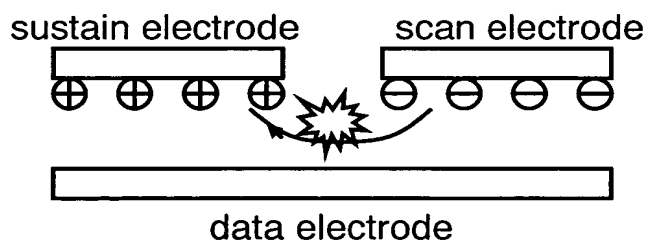
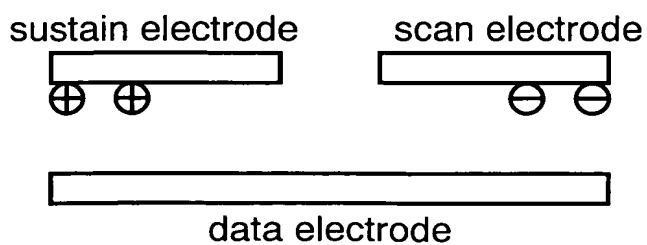


FIG. 6D



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FIG. 7A

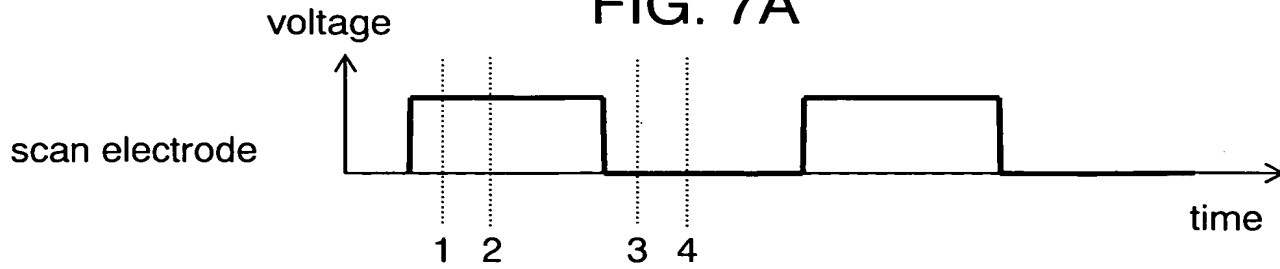


FIG. 7B

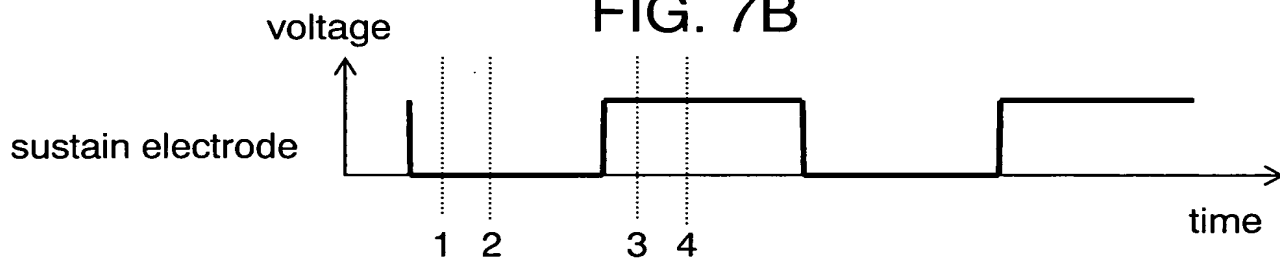


FIG. 7C

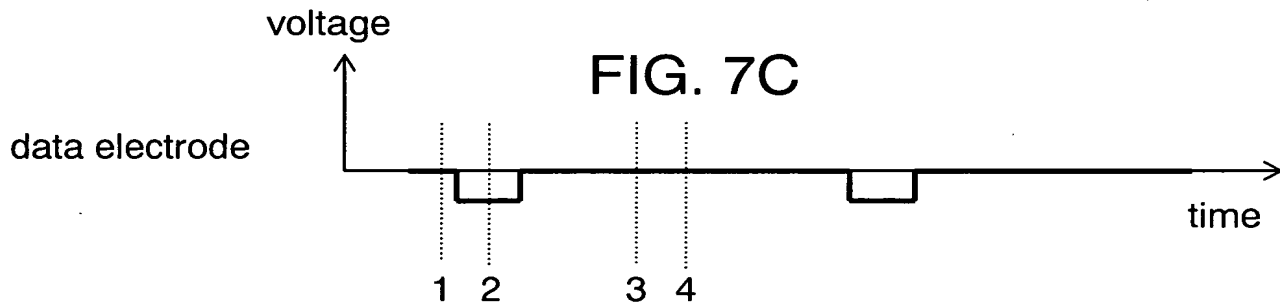


FIG. 7D

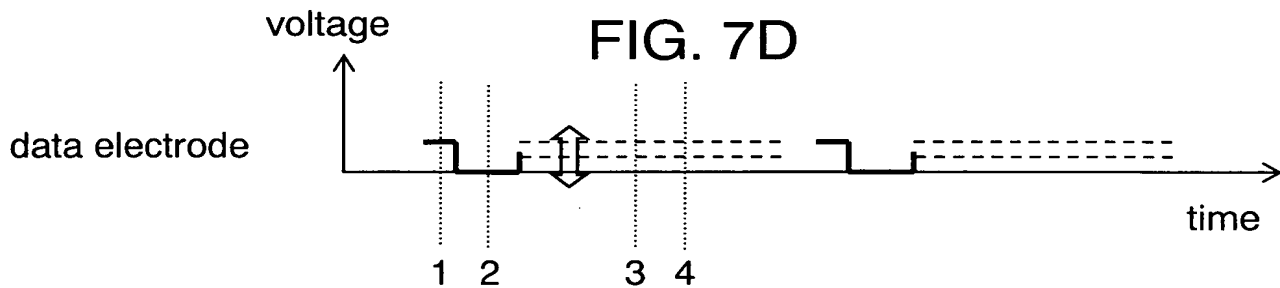
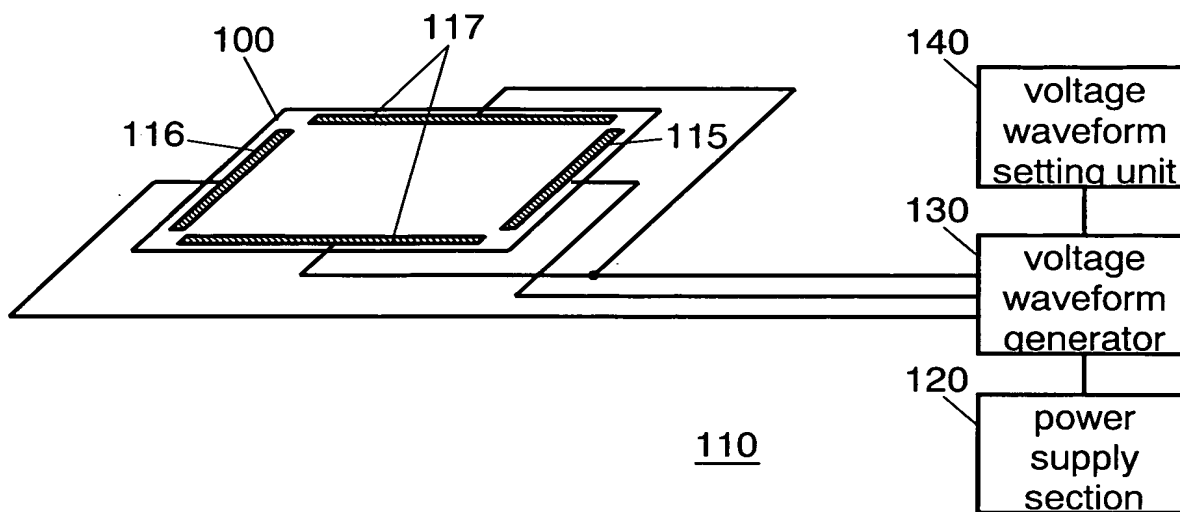


FIG. 7E



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FIG. 8



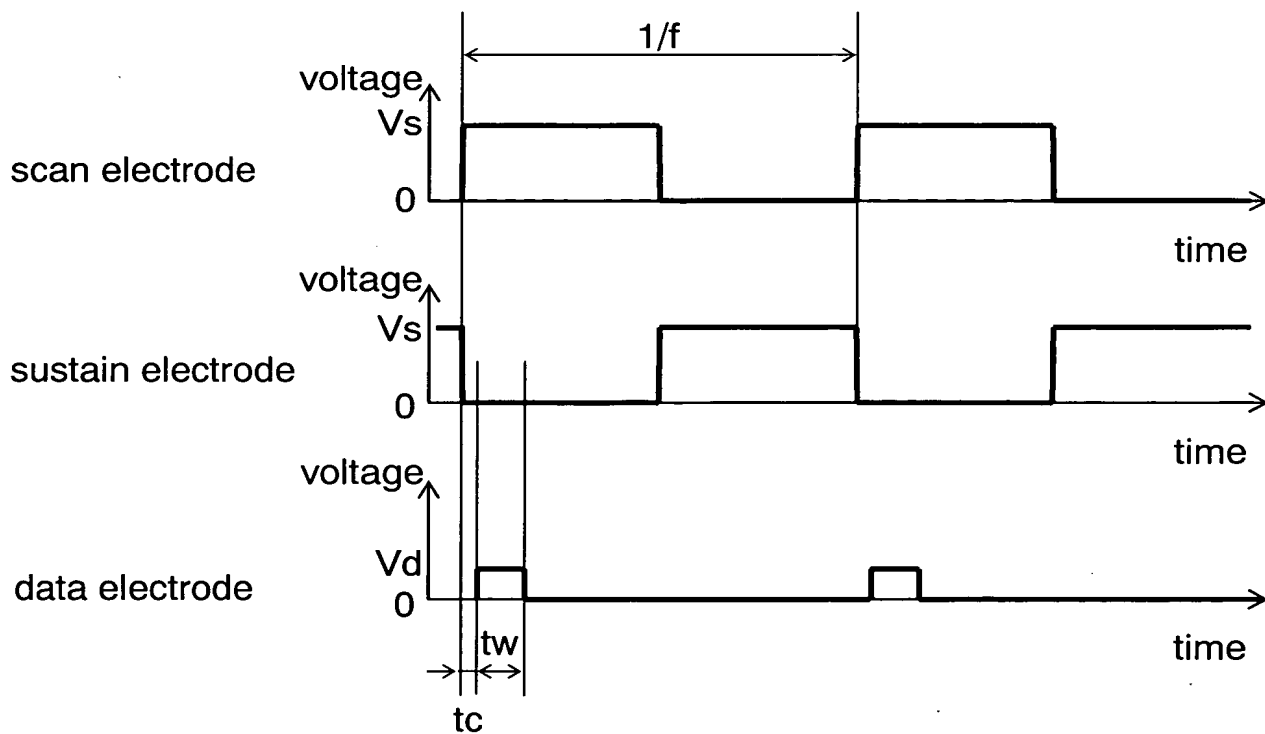


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FIG. 9A

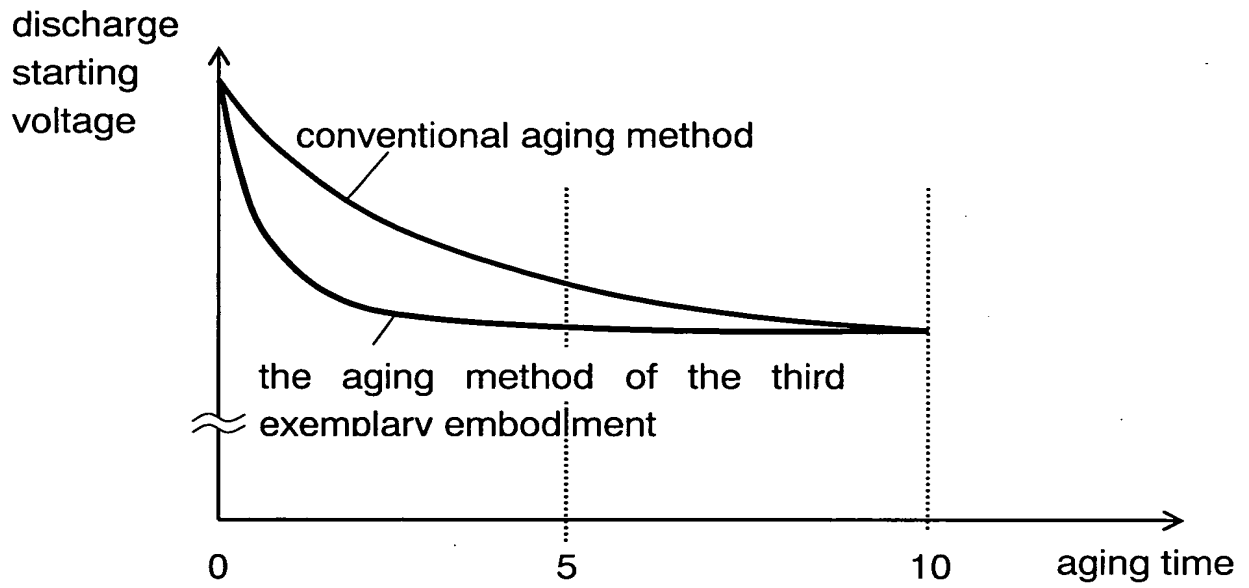
setting data		up	down	setting	
aging time	<b>T</b>	⬆	⬇	8888	min
aging voltage	<b>Vs</b>	⬆	⬇	8888	Volt
pulse-repetition frequency	<b>f</b>	⬆	⬇	8888	kHz
voltage applied to the data electrode	<b>Vd</b>	⬆	⬇	8888	Volt
pulse width	<b>tw</b>	⬆	⬇	8888	$\mu$ s
time delay	<b>tc</b>	⬆	⬇	8888	ns

FIG. 9B



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FIG. 10



## Reference numerals in the drawings

1, 100:	plasma display panel (panel)
2:	front substrate
3:	back substrate
4:	front glass plate
5, 5 <sub>1</sub> – 5 <sub>n</sub> :	scan electrode
6, 6 <sub>1</sub> – 6 <sub>n</sub> :	sustain electrode
7:	dielectric layer
8:	protecting layer
9:	back glass plate
10, 10 <sub>1</sub> – 10 <sub>m</sub> :	data electrode
11:	dielectric layer
12:	barrier rib
13:	phosphor layer
15, 15 <sub>1</sub> – 15 <sub>n</sub> :	scan electrode terminal section
16, 16 <sub>1</sub> – 16 <sub>n</sub> :	sustain electrode terminal section
17, 17 <sub>1</sub> – 17 <sub>n</sub> :	data electrode terminal section
18:	discharge cell
20:	discharge gap
21:	adjacent gap
100:	plasma display panel
110:	aging device
115, 116, 117:	short-circuit bar
120:	power supply section
130:	voltage waveform generator
140:	voltage waveform setting unit